

CH32V3xx Evaluation Board Reference

Version: V1.9

<https://wch-ic.com>

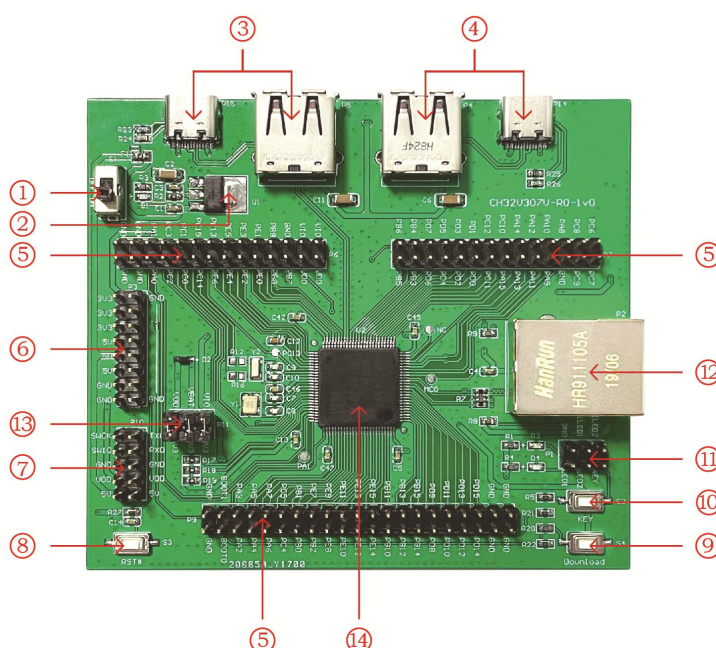
1. Overview

This evaluation board is applied to the development of the CH32V30x/CH32V317 chip. The IDE uses the MounRiver compiler, with the option of using the on-board or independent WCH-Link for emulation and download, and provides reference examples and demonstrations of chip resource-related applications.

2. Evaluation Board Hardware

Please refer to the CH32V307SCH.pdf document for the schematic of the evaluation board.

CH32V307 Evaluation Board



Descriptions

1.Power switch	5.MCU I/O port	9.Download button	13.MCU power supply row pin
2.Voltage regulator chip	6.Power supply row pin	10.KEY	14.Main control MCU
3.USB interface	7.DUBUG interface	11.KEY and LED row pin	
4. USB interface	8.Reset button	12.Network port	

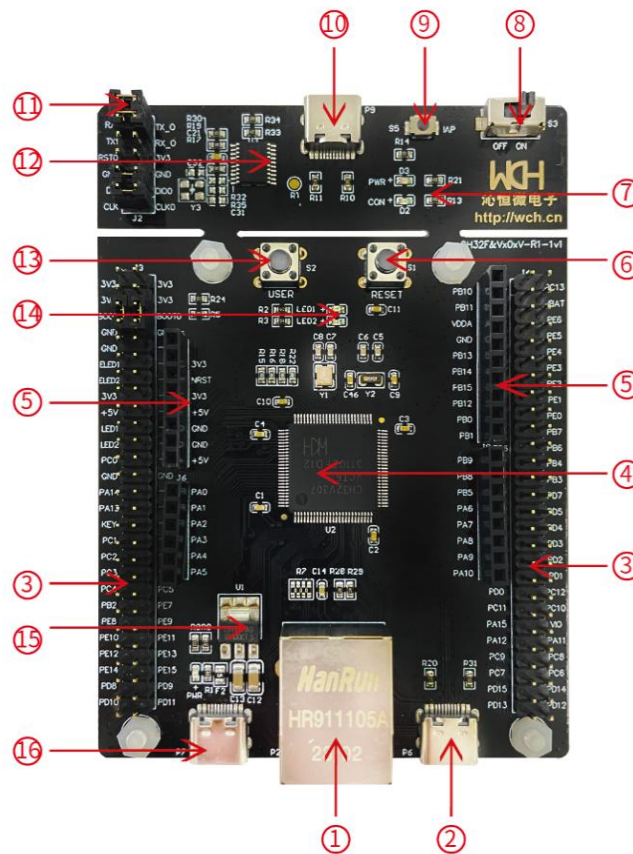
The above CH32V307 evaluation board comes with the following resources.

Motherboard - CH32V307EVT

1. Switch S1: Used to disconnect or connect external 5V power supply or USB power supply
2. Forward low dropout voltage regulator chip U1: used to realize the conversion of 5V voltage to 3.3V supply voltage available to the chip
3. USB interface P5, P15: USB communication interface PB6, PB7 of the main chip
4. USB interface P4, P14: USB communication interface PA11, PA12 of the main chip
5. MCU I/O ports P6, P7, P9: I/O pinout interface of the main control MCU
6. Power supply row pin P3: 5V, 3.3V, GND external power supply row pin

7. DEBUG interface P10: for downloading, emulation debugging
8. Button S3: Reset button for external manual reset of the main control MCU
9. Button S4: Download button, used to start download from BOOT
10. Key S2: Connects to the I/O port of the main MCU through the P1 row of pins for key control
11. KEY and LED row pin P1: P1 row pin connects to the I/O of the main control MCU to control LED and KEY
12. Network port: Network communication interface of the main chip
13. MCU power supply row pin P11: for main control MCU power supply selection
14. Main control MCU: CH32V307VCT6

CH32V307 Evaluation Board



Descriptions

- | | | | |
|--------------------------------|----------------------|-------------------------|------------------------------|
| 1. Network port | 5. ARDUINO interface | 9. WCH_LinkE IAP button | 13. USER button |
| 2. USB2.0 high-speed interface | 6. Reset button | 10. WCH_LinkE interface | 14. LED |
| 3. MCU I/O | 7. WCH-Link LED | 11. SDI&UART interface | 15. Voltage regulator |
| 4. Main control MCU | 8. Power switch | 12. WCH-LinkE MCU | 16. USB full-speed interface |

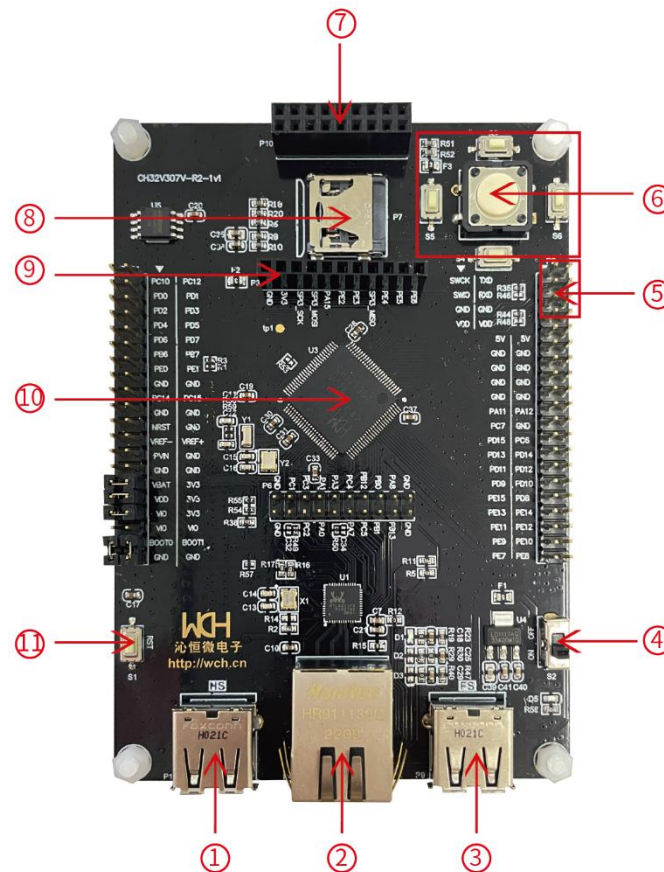
The CH32V307V evaluation board shown above comes with the following resources.

Motherboard - CH32V307EVT-R1

1. Network port: Network communication interface of the main chip
2. USB interface P6: Connected to the main chip USB high-speed communication interface
3. MCU I/O port: I/O output interface of main control MCU
4. Main control MCU: CH32V307VCT6
5. ARDUINO interface: Easy to connect development boards with ARDUINO interface

6. Reset button: For external manual reset of the main MCU
7. WCH-LinkE indicator: Indicates WCH-LinkE operation status
8. Power switch S3: For disconnecting or connecting external 5V power supply or USB power supply.
9. WCH_LinkE IAP Button: WCH_LinkE Upgrade Button
10. WCH-LinkE interface: For connection between PC and WCH-LinkE function module
11. SDI&UART interface: Used for download, simulation and debugging, need jumper to choose whether to use onboard WCH-LinkE or not.
12. WCH-LinkE MCU: MCU that realizes WCH-LinkE function.
13. USER button S2: Connect the I/O port of the main MCU through J3 pin for key control
14. LED: Connected to the IO port of the main MCU through J3 pin for control
15. Voltage regulator chip U1: Used to realize the conversion of 5V voltage to 3.3V supply voltage available to the chip.
16. USB interface P7: Connects to the main chip USB full-speed communication interface

CH32V307 Evaluation Board



Descriptions

- | | | | |
|-----------------------------|--------------------|---------------------|----------------------|
| 1. USB high-speed interface | 4. Power switch | 7. Camera interface | 10. Main control MCU |
| 2. Network port | 5. DEBUG interface | 8. SD card holder | 11. Reset button |
| 3. USB full-speed interface | 6. User button | 9. Screen interface | |

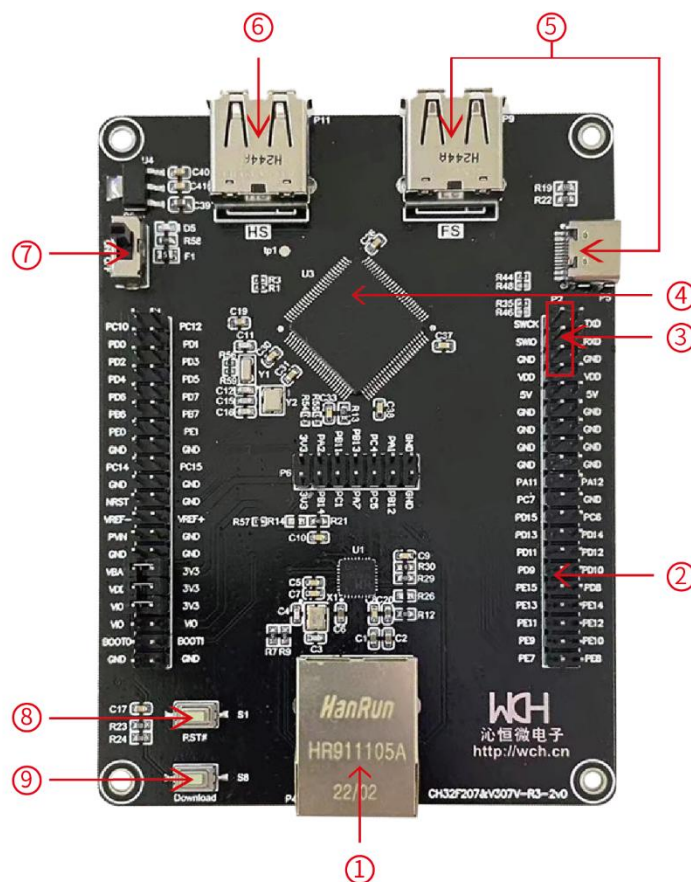
The CH32V307V-R2 EVT board comes with the following resources.

Motherboard - CH32V307V-R2

1. USB interface P11: Connects to the main chip USB high-speed communication interface

2. Network port: Network communication interface (Gigabit) of the main chip
3. USB interface P9: Connects to the main chip USB full-speed communication interface
4. Power switch S2: Used to disconnect or connect external 5V power supply or USB power supply.
5. Debugging interface: For downloading, simulation debugging
6. Button: User button
7. Camera interface P10: Realize MCU external camera (DVP-8bits)
8. SD Card Holder P7: Connects to the SDIO interface and demonstrates the operation of a TF card through the SDIO interface.
9. Screen interface P3: Realize MCU external SPI port display screen
10. Main control MCU: CH32V307VCT6
11. Reset button S1: For external manual reset of the main control MCU

CH32V307 Evaluation Board



Descriptions

- | | | |
|--------------------|-----------------------------|--------------------|
| 1. Network port | 4. Main control MCU | 7. Power switch |
| 2. MCU I/O | 5. USB full-speed interface | 8. Reset button |
| 3. Debug interface | 6. USB high-speed interface | 9. Download button |

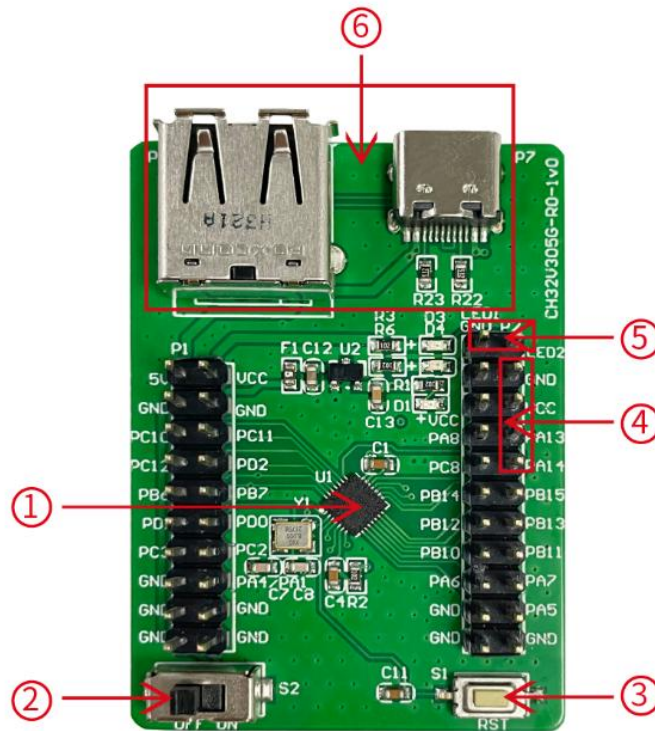
The CH32V307V-R3 EVT board comes with the following resources.

Motherboard - CH32V307EVT-R3

1. Network port: Network communication interface of the main chip (100MB)
2. MCU I/O port: I/O port of the main control MCU.
3. Debugging interface: For downloading, simulation debugging

4. Main control MCU: CH32V307VCT6
5. USB interface P9, P5: Connect to the main chip USB full-speed communication interface
6. USB interface P11: Connect to the main chip USB high-speed communication interface
7. Power switch S2: Used to disconnect or connect external 5V power supply or USB power supply.
8. Button S1: Reset button for external manual reset of the main MCU.
9. Button S8: Download button, used to start the download from the BOOT

CH32V305 Evaluation Board



Descriptions

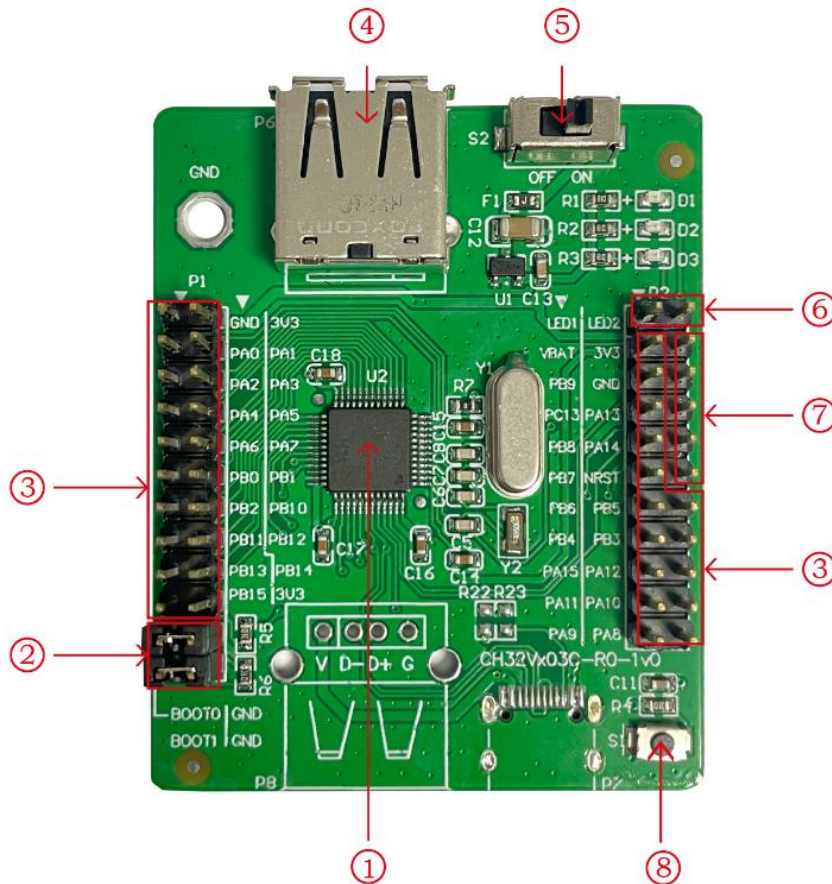
- | | | |
|--------------------|-----------------|------------------|
| 1. MCU | 2. Power switch | 3. Reset button |
| 4. Debug interface | 5. LED | 6. USB interface |

The CH32V305 EVT board comes with the following resources.

Motherboard - CH32V305EVT

1. Main control MCU: CH32V305GBU6
2. Power switch S2: Used to disconnect or connect external 5V power supply or USB power supply.
3. Button S1: Reset button for external manual reset of the main MCU.
4. Debugging interface: For downloading, simulation debugging
5. LED: Controlled by connecting to the IO port of the main chip via pins
6. USB interface P7, P8: USB communication interface PB6, PB7 of the main chip

CH32V303 Evaluation Board



Descriptions

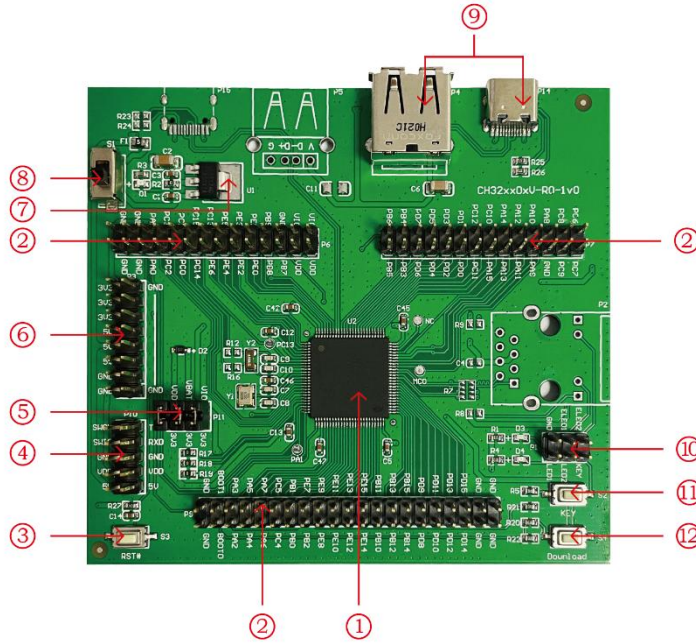
- | | | |
|---------------------------|-----------------|-------------------|
| 1.MCU | 4.USB interface | 7.DEBUG interface |
| 2.Boot mode configuration | 5.Power switch | 8.Reset button |
| 3.MCU I/O | 6.LED | |

The above CH32V303 evaluation board comes with the following resources.

Motherboard - CH32V303EVT

1. Main control MCU: CH32V303CBT6
2. Boot mode configuration P3: Select the boot mode when the chip is powered on by configuring BOOT0/1
3. MCU I/O port P1, P2: I/O pinout interface of the main control MCU
4. USB interface P6: USB communication interface PB6, PB7 of the main chip
5. Switch S2: Used to disconnect or connect external 5V power supply or USB power supply
6. LED: Connected to the main chip I/O port via pins for control
7. DEBUG interface: for downloading, simulation debugging
8. Button S1: Reset button for external manual reset of the main control MCU

CH32V303 Evaluation Board



Descriptions

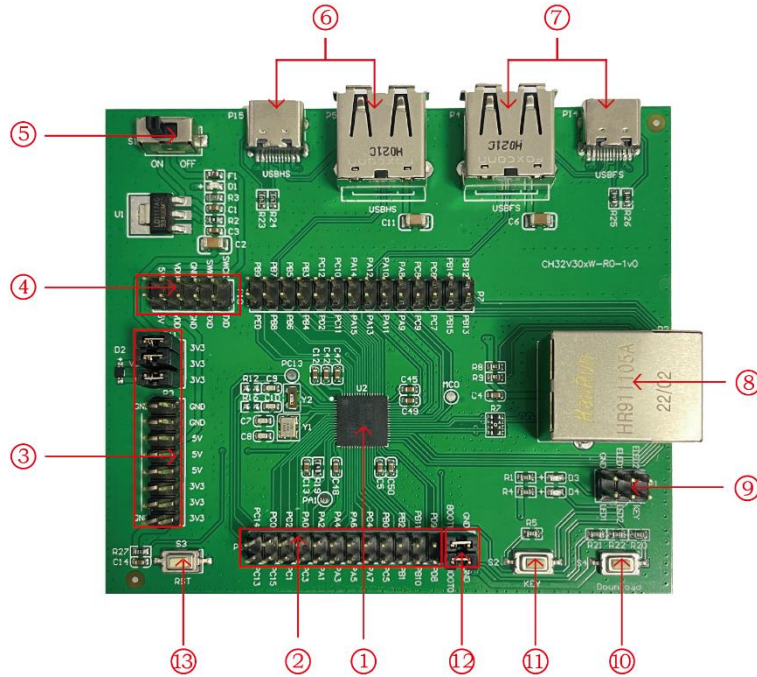
- | | | | |
|---------------------|-----------------------------|---------------------------|-------------------------|
| 1. Main control MCU | 4. DEBUG interface | 7. Voltage regulator chip | 10. KEY and LED row pin |
| 2. MCU I/O | 5. MCU power supply row pin | 8. Power switch | 11. KEY |
| 3. Reset button | 6. Power supply row pin | 9. USB interface | 12. Download button |

The above CH32V303 evaluation board comes with the following resources.

Motherboard - CH32V303EVT

1. Main control MCU: CH32V303VCT6
2. MCU I/O ports P6, P7, P9: I/O pinout interface of the main control MCU
3. Button S3: Reset button for external manual reset of the main control MCU
4. DEBUG interface P10: for downloading, emulation debugging
5. MCU power pin P11: for main MCU power supply selection
6. Power supply pin P3: 5V, 3.3V, GND external power supply pin
7. Forward low dropout voltage regulator chip U1: used to realize the conversion of 5V voltage to 3.3V supply voltage available to the chip
8. Switch S1: Used to disconnect or connect external 5V power supply or USB power supply
9. USB interface P4, P14: USB communication interface PA11, PA12 of the main chip
10. KEY and LED row pin P1: P1 row pin connects to the I/O of the main control MCU to control LED and KEY
11. Key S2: Connects to the I/O port of the main MCU through the P1 row of pins for key control
12. Button S4: Download button, used to start download from BOOT

CH32V303 Evaluation Board



Descriptions

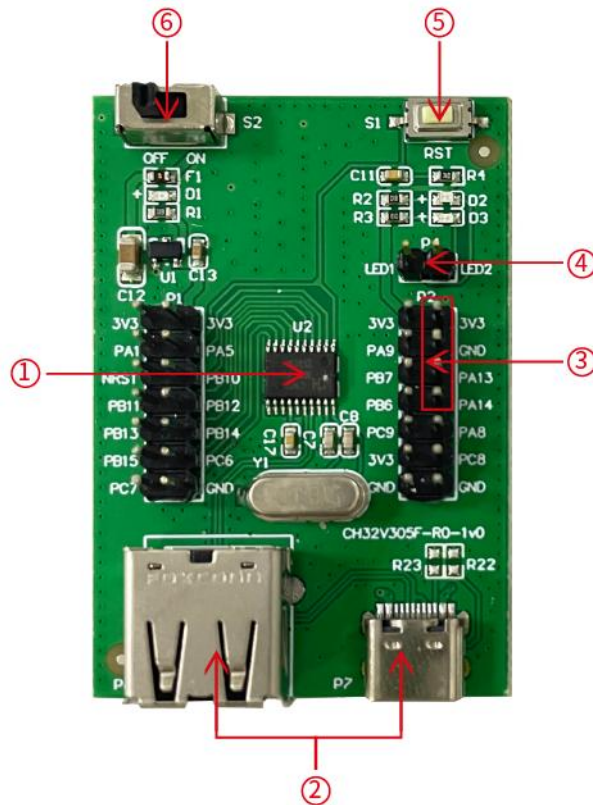
- | | | | |
|-------------------------|--------------------|------------------------|-----------------------------|
| 1. Main control MCU | 4. DEBUG interface | 7. USB interface | 10. Download button |
| 2. MCU I/O | 5. Power switch | 8. Network port | 11. KEY |
| 3. Power supply row pin | 6. USB interface | 9. KEY and LED row pin | 12. Boot mode configuration |
| | | | 13. Reset button |

The above CH32V30x evaluation board comes with the following resources.

Motherboard - CH32V30xEVT

1. Main control MCU: CH32V303WCU6
2. MCU I/O port P6, P7: I/O pinout interface of the main control MCU
3. Power supply row pin: for the main control MCU power supply selection
4. DEBUG interface P10: for downloading, emulation debugging
5. Switch S1: Used to disconnect or connect external 5V power supply or USB power supply
6. USB interface P5, P15: USB communication interface PB6, PB7 of the main chip
7. USB interface P4, P14: USB communication interface PA11, PA12 of the main chip
8. Network port: Network communication interface of the main chip
9. KEY and LED row pin P1: P1 row pin connects to the IO of the main MCU to control LED and KEY
10. Button S4: Download button, used to start download from BOOT
11. Key S2: Connects to the I/O port of the main control MCU through the P1 row of pins for key control
12. Boot mode configuration: Select the boot mode when the chip is powered on by configuring BOOT0/1
13. Button S3: Reset button for external manual reset of the main control MCU

CH32V305 Evaluation Board



Description

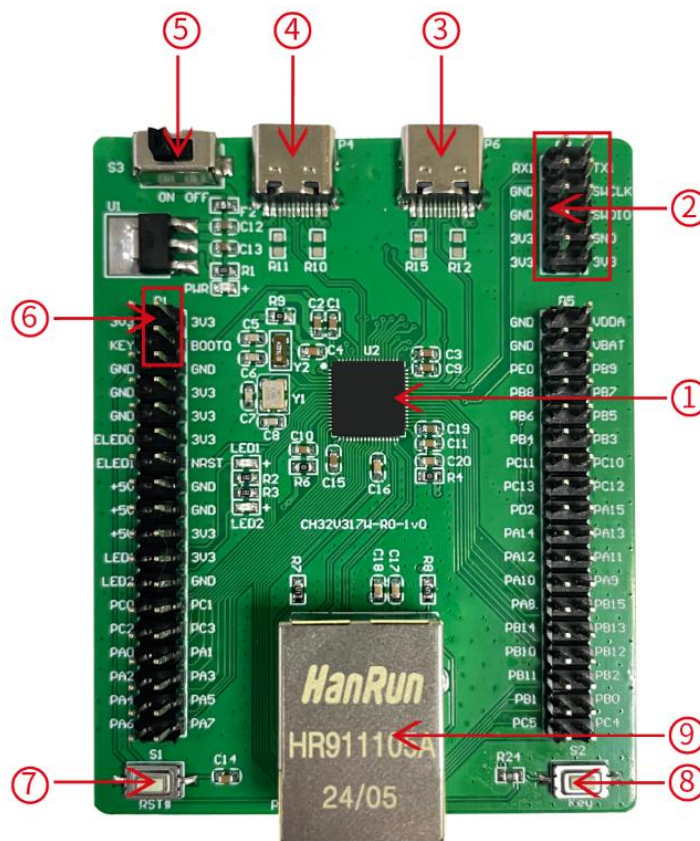
- | | | |
|---------------------|-------------------|--------------------|
| 1. Main control MCU | 2.. USB interface | 3. DEBUG interface |
| 4. LED pin | 5. Reset button | 6. Power switch |

The above CH32V30x evaluation board comes with the following resources:

Motherboard - CH32V30xEVT

1. Main control MCU: CH32V305FBP6
2. USB interface: USB communication interface of the main chip
3. DEBUG interface: For downloading, emulation debugging
4. LED pin: The pin is connected to the IO of the main control MCU to control the LED.
5. Reset button: Used for external manual reset of the main control MCU
6. Power switch: Used to cut off or connect external 5V power supply or USB power supply.

CH32V317 Evaluation Board



Descriptions

- | | | | |
|---------------------|----------------------------|-----------------|----------|
| 1. Main control MCU | 2. DEBUG interface | 3. USBFS | 4. USBHS |
| 5. Power switch | 6. Boot mode configuration | 7. Reset button | 8. Key |
| 9. Network port | | | |

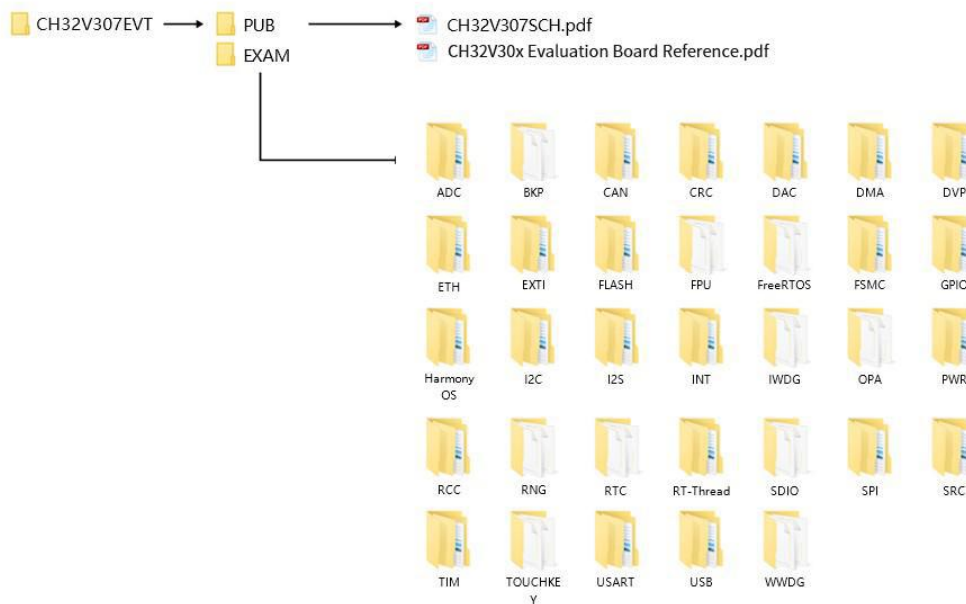
The above CH32V317 evaluation board comes with the following resources:

Motherboard - CH32V317EVT

1. Main control MCU: CH32V317WCU6
2. DEBUG interface: For downloading, emulation debugging
3. USBFS: USBFS communication interfaces PA11, PA12
4. USBHS: USBHS communication interface PB6, PB7
5. Power switch: Used to cut off or connect external 5V power supply or USB power supply.
6. Boot mode configuration: Configure BOOT0 to select the boot mode when the chip is powered on.
7. Reset button: Used for external manual reset of the main control MCU
8. Key: Connect to the IO port of the main MCU through the key pin for key control.
9. Network port: The network communication interface of the main chip.

3. Software Development

3.1 EVT Package Directory Structure



Description.

PUB folder: provides evaluation board manuals, evaluation board schematics.

EXAM folder: Provides software development drivers and corresponding examples for the CH32V307 controller, grouped by peripheral. Each type of peripheral folder contains one or more functional application routines folders.

3.2 IDE Use -MounRiver

Download MounRiver_Studio, double click to install it, and you can use it after installation. (MounRiver_Studio instructions are available at the path: MounRiver\MounRiver_Studio\MounRiver_Help.pdf and MounRiver_ToolbarHelp.pdf)

3.2.1 Open Project

➤ Open project:

- 1) Double-click project file directly with the suffix name .wvproj under the corresponding project path.
- 2) Click File in MounRiver IDE, click Load Project, select the .project file under the corresponding path, and click Confirm to apply it.

3.2.2 Compilation

MounRiver contains three compilation options, as shown in the following figure.



Compile option 1 is Incremental Build, which compiles the modified parts of the selected project.

Compile option 2 is ReBuild, which performs a global compilation of the selected project.

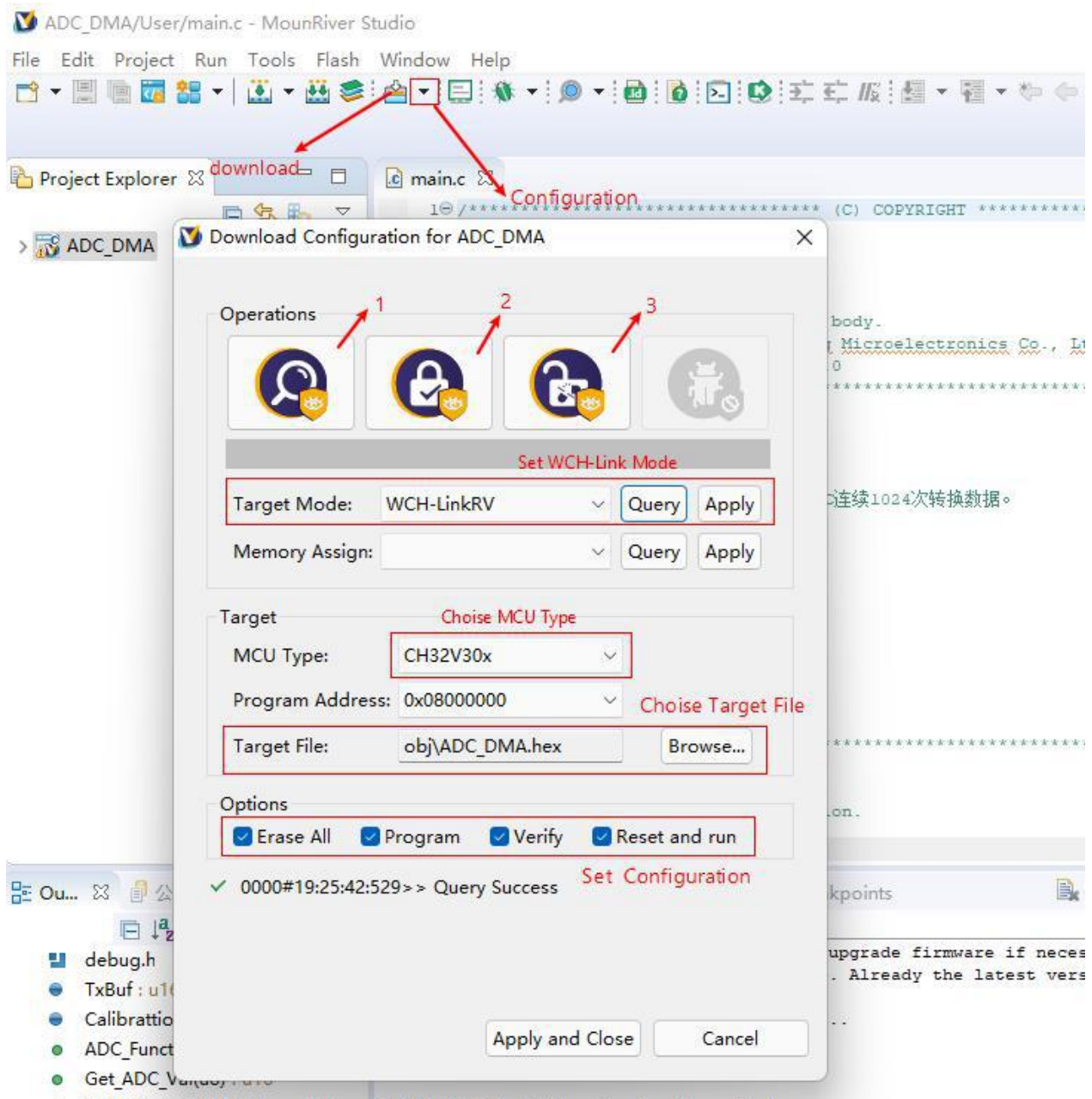
Compile option 3 is All Build, which performs global compilation for all projects.

3.2.3 Download/Simulation

➤ Download

1) Debugger download

Connect to the hardware via WCH-Link (see WCH-Link instructions for details, path: MounRiver\MounRiver_Studio\ WCH-Link instructions.pdf), click the Download button on the IDE, and select Download in the pop-up interface, as shown in the figure below.



1 for querying the chip read protection status.

2 for setting the chip read protection and re-powering the configuration to take effect.

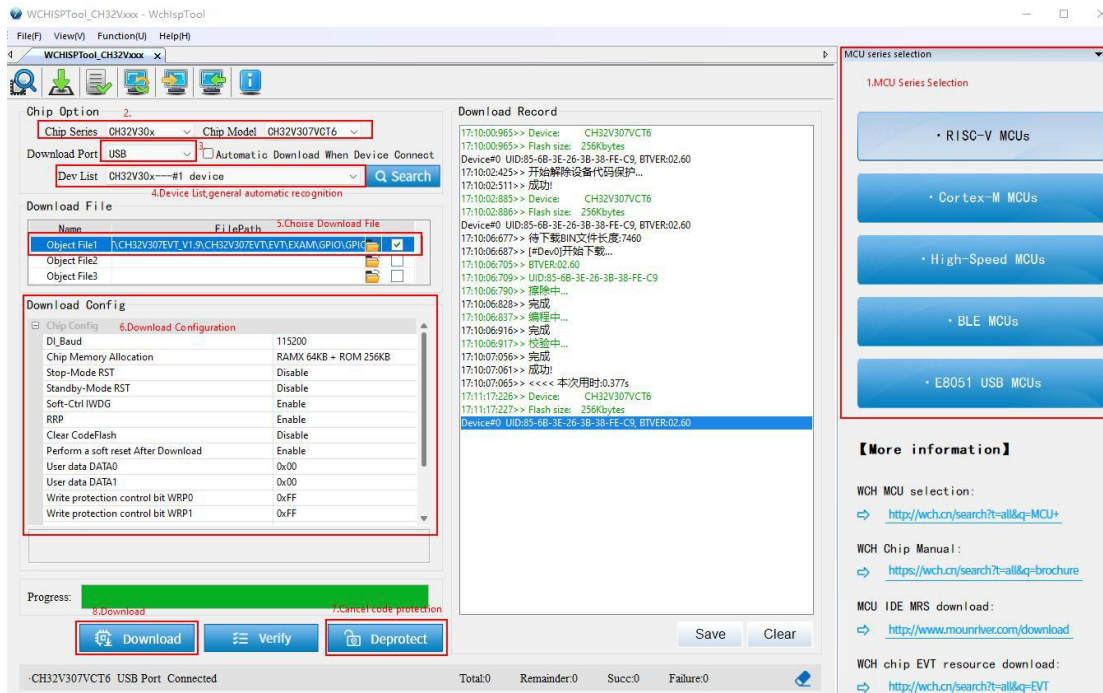
3 for lifting the chip read protection and re-powering the configuration to take effect.

2) WCHISPTool Download

The WCHISPTool tool is used to download the chip, supporting both USB and serial port. the USB pins are PA11 (DM), PA12 (DP) or PB6 (DM), PB7 (DP), and the serial port pins are PA9 (TX), PA10 (RX). The download process is.

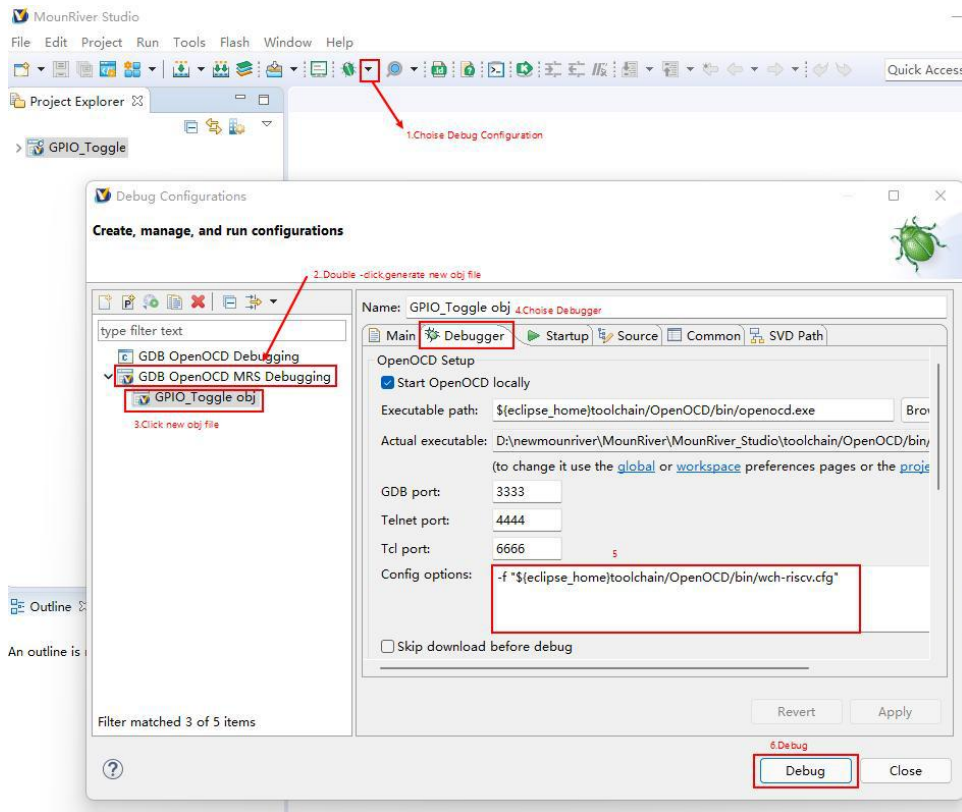
- (1) BOOT0 to VCC and BOOT1 to ground, connected to PC via serial or USB.
- (2) Open the WCHISPTool tool, select the appropriate download method, choose to download the firmware, check the chip configuration and click download.
- (3) BOOT0 is grounded, re-powered and running the APP program.

The WCHISPTool tool interface is shown in the following figure.



➤ Simulation

Open MounRiver Studio software for debugging configuration



1) Toolbar description

Click Debug button in the menu bar to enter the download, see the image below, the download toolbar.



Detailed functions are as follows.

1. Restart: After reset, the program returns to the beginning.
2. Continue: Click to continue debugging.
3. Terminate: Click to exit debugging.
4. Single-step jump-in: For each key click, the program runs one step and encounters a function to enter and execute.
5. Single-step skip: jump out of the function and prepare the next statement.
6. Single-step return: return to the function that was jumped into
7. Instruction set single-step mode: click to enter instruction set debugging (need to use with 4, 5 and 6 functions).

2) Set breakpoints

Double-click on the left side of the code to set a breakpoint, double click again to cancel the breakpoint, set the breakpoint as shown in the following figure;

```

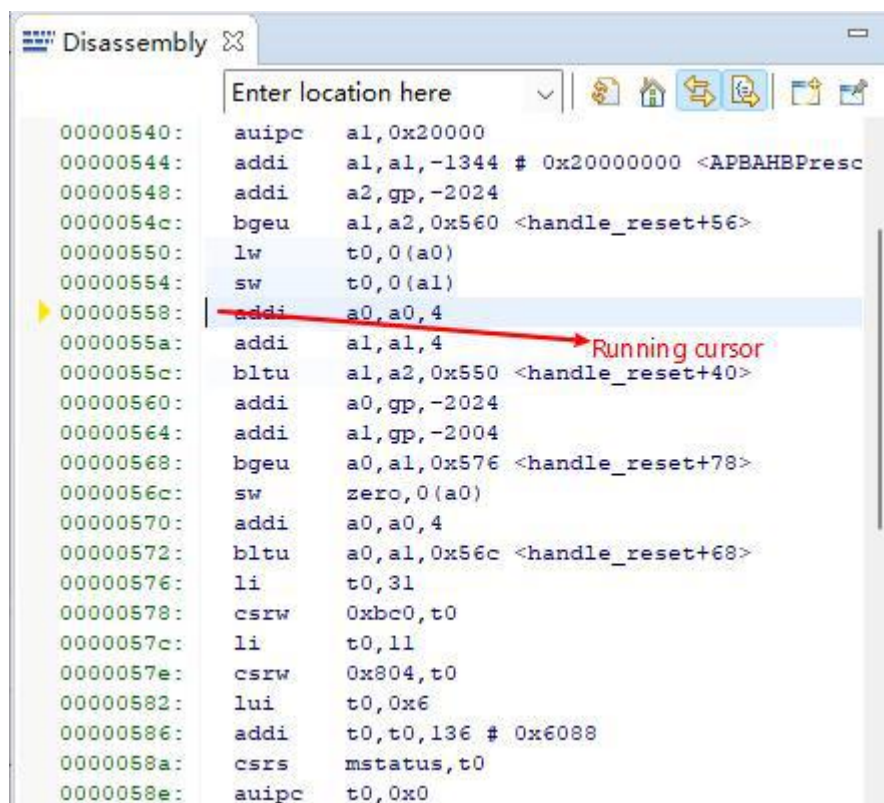
132  * @return none
133  */
134  int main(void)
135  {
136      uint i;
137      Delay_Init();
138      USART_Printf_Init(115200);
139      printf("SystemClk:%d\r\n", SystemCoreClock);
140
141      ADC_Function_Init();
142
143      DMA_Tx_Init(DMA1_Channel1, (uint32_t)&ADC1->RDATAR, (uint32_t)TxBuf, 10);
144      DMA_Cmd(DMA1_Channel1, ENABLE);
145
146      ADC_RegularChannelConfig(ADC1, ADC_Channel_2, 1, ADC_SampleTime_241Cycles);
147      ADC_SoftwareStartConvCmd(ADC1, ENABLE);
148      Delay_Ms(50);
149      ADC_SoftwareStartConvCmd(ADC1, DISABLE);
150

```

3) Interface display

(1) Instruction set interface

Click on the instruction set single-step debugging can enter the instruction debugging, to single-step jump in for example, click once to run once, the running cursor will move to view the program running, the instruction set interface is shown as follows.



(2) Program running interface

It can be used with instruction set single-step debugging, still take single-step jumping in as an example, click once to run once, the running cursor will move to view the program running, the program running interface is shown as follows.

```

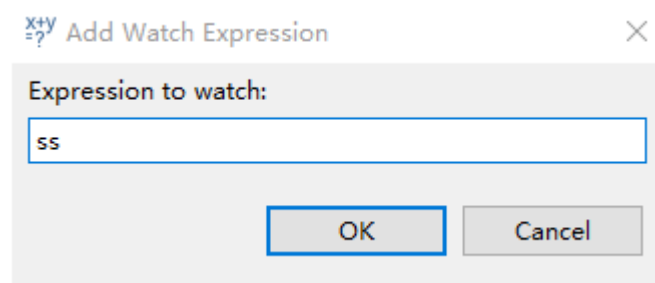
46  * @return none
47  */
48
49  int main(void)
50  {
51      u8 i = 0;
52
53      NVIC_PriorityGroupConfig(NVIC_PriorityGroup_2);
54      Delay_Init();
55      USART_Printf_Init(115200);
56      printf("SystemClk:%d\r\n", SystemCoreClock);
57
58      printf("GPIO Toggle TEST\r\n");
59      GPIO_Toggle_INIT();
60
61      while(1)
62      {
63          Delay_Ms(250);
64          GPIO_WriteBit(GPIOD, GPIO_Pin_0, (i == 0) ? (i = Bit_SET) : (i = Bit_RESET));
65      }
66  }
67

```

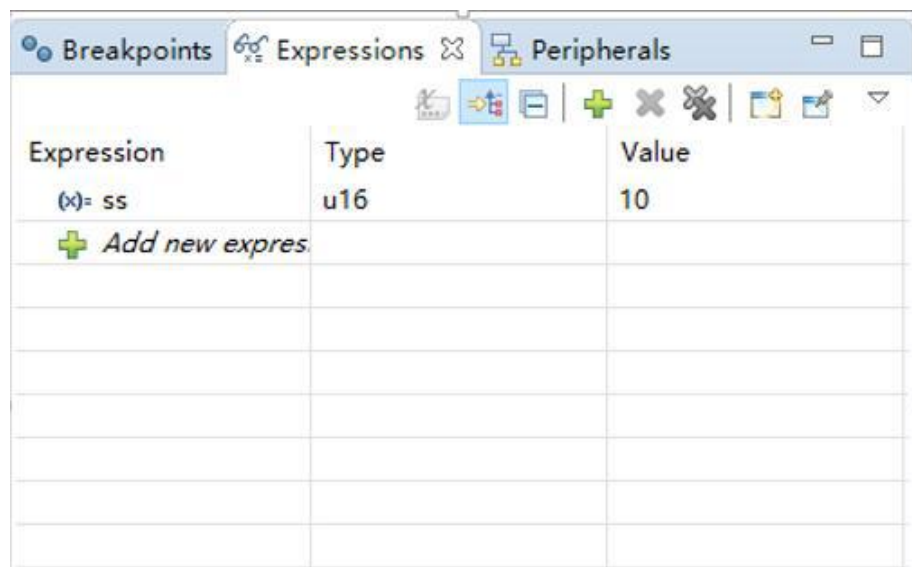
Running cursor

4) Variables

Hover over the variable in the source code to display the details, or select the variable and right-click add watch expression

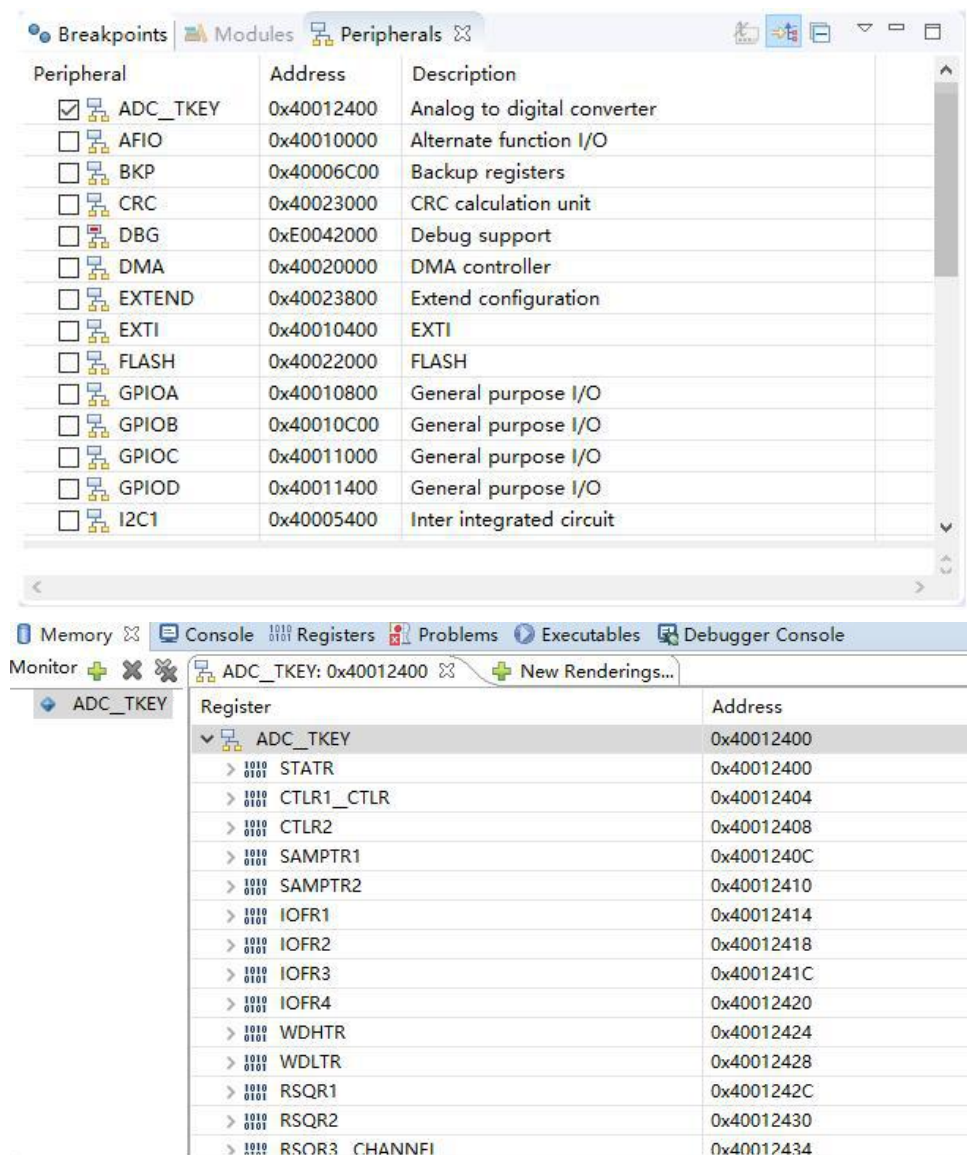


Fill in the variable name, or just click OK to add the variable you just selected to the pop-up.



5) Peripheral registers

In the lower left corner of IDE interface Peripherals interface shows a list of peripherals, tick the peripherals will display its specific register name, address, value in the Memory window.



Note:

(1) When debugging, click the icon in the upper right corner to enter the original interface.

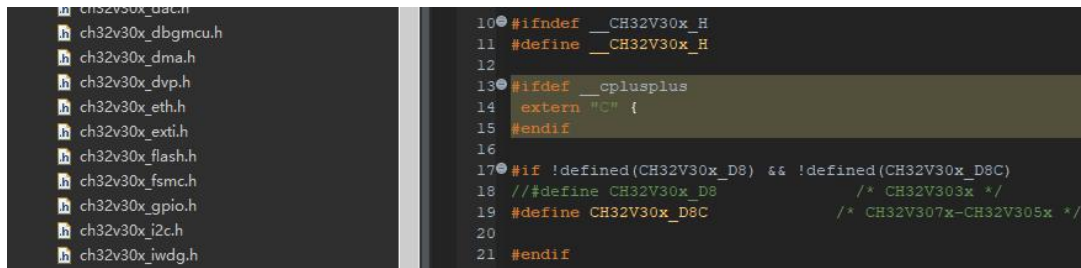


(2) For documentation to access the compiler, click F1 to access the help documentation for detailed instructions.

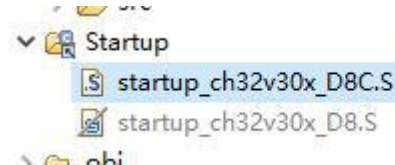
➤ Engineering Chip Selection

In the course of the project there are a variety of chip selection, to CH32V307 development board used chip CH32V307VCT6 as an example of engineering chip selection compilation, has achieved different peripheral functions, the steps are as follows.

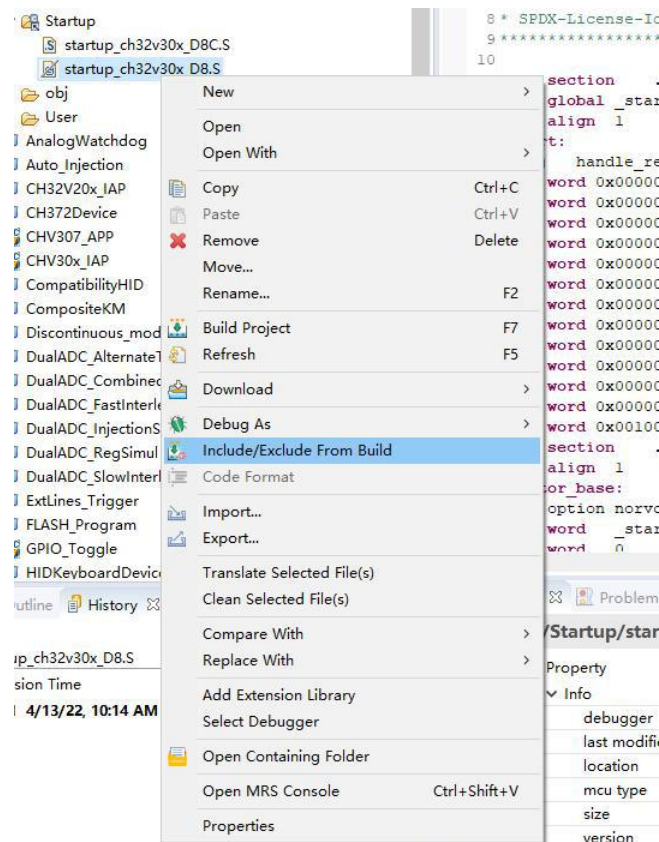
- 1) Click Peripheral-> inc folder ch32v30x.h file to check the chip type, as shown below, because the chip type is CH32V307VCT6, so choose CH32V30x_D8C. (Note: If you use CH32V303x series chip choose CH32V30x_D8)



- 2) Select the startup file, click on the Startup folder, select the corresponding startup file defined, as shown below, because step 1 select CH32V30x_D8C, so select the startup_ch32v30x_D8C.S file (Note: If using CH32V303x series chips select startup_ch32v30x_D8.S)



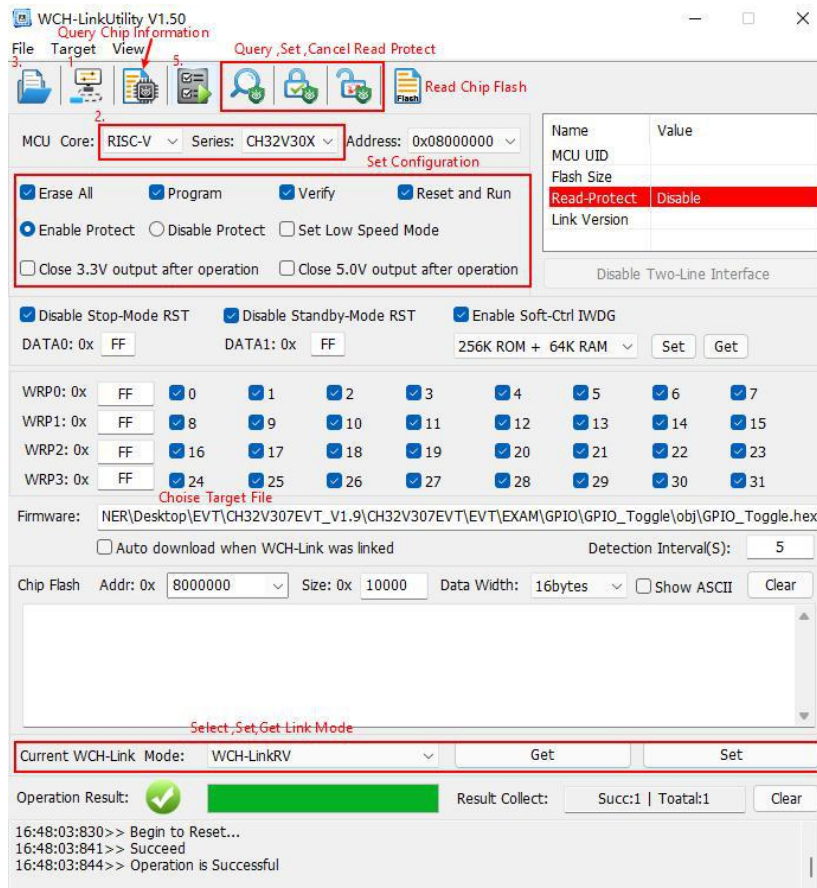
Note: In the project is not used in the file, such as in the folder file click delete, will lead to the file directly deleted to the return station, re-use needs to recompile to be able to, so for no use of the file is recommended to use the participate / exclude compile function, to Startup folder compile / exclude a brief description. First of all, select the project right click, select the compile involved/excluded function button, take the startup_ch32v30x_D8.S file in the figure as an example, if the project status is excluded compile, click this function can be involved in compile. Anyway, if the project status is compile-in, click this function to compile-out. The same applies to folders.



4. WCH-LinkUtility.exe Download

The download process for the chip using the WCH-LinkUtility tool is:

- 1) Connect WCH-Link
- 2) Select chip information
- 3) Add firmware
- 4) If the chip is read protected, you need to release the chip read protection.
- 5) Execute

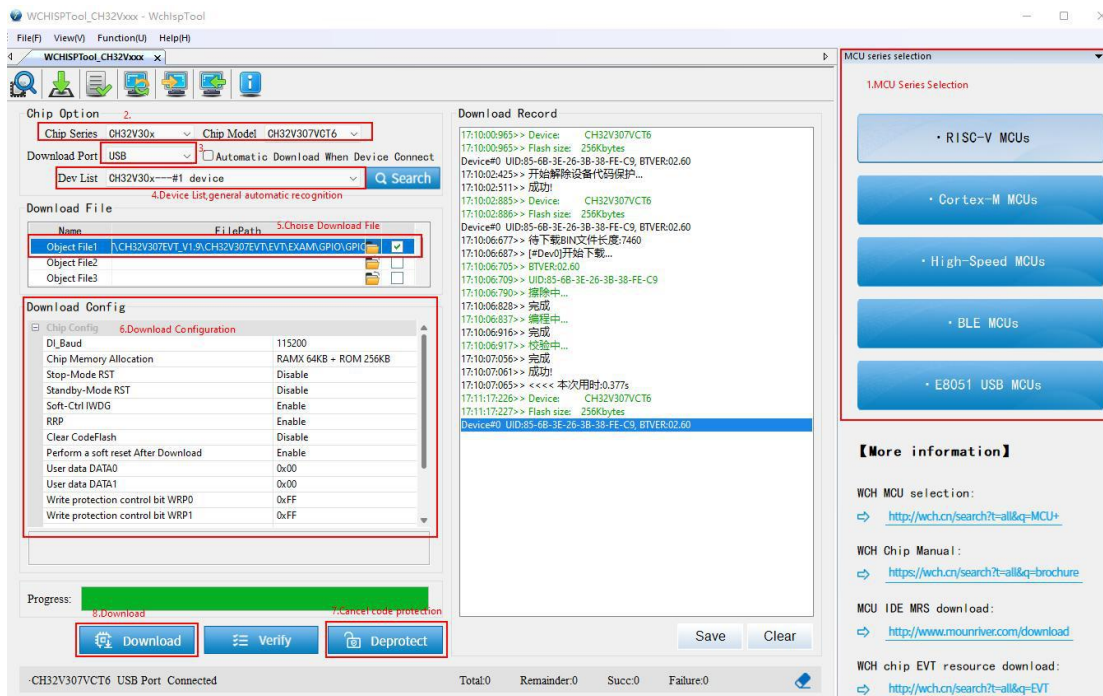


5. WCHISPTool.exe Download

The WCHISPTool tool is used to download the chip, supporting both USB and serial port. the USB pins are PA11 (DM), PA12 (DP) or PB6 (DM), PB7 (DP), and the serial port pins are PA9 (TX), PA10 (RX). The download process is:

- 1) BOOT0 to VCC and BOOT1 to ground, connected to PC via serial port or USB.
- 2) Open the WCHISPTool tool, select the appropriate download method, choose to download the firmware, check the chip configuration and click on download.
- 3) BOOT0 is grounded, re-powered and running the APP program.

The WCHISPTool tool interface is shown in the following figure.



1. Select MCU series and chip model
2. Select the serial port download mode
3. Identify the device, usually automatically, if it fails to identify, you need to select manually
4. Select the firmware, select the downloaded .hex or .bin target program file
5. Configure the download according to the requirements
6. Click download

6. Statement of Attention

WCH official website: <https://www.wch-ic.com/>

WCH-LINK instructions for use: <https://www.wch-ic.com/products/WCH-Link.html>